

OsmoSDR - Issues

| # | Tracker | Status | Priority | Subject | Assignee | Updated | Related issues |
|------|---------|----------|-----------|---|----------|---------------------|----------------|
| 4938 | Feature | Rejected | Normal | Telephone design | efistokl | 01/08/2021 03:56 PM | |
| 4937 | Feature | Rejected | Normal | Beginner | | 01/08/2021 03:56 PM | |
| 1473 | Feature | Rejected | High | verify if 80Mhz rate between ADC and FPGA works | laforge | 02/20/2016 08:23 PM | |
| 1472 | Feature | Rejected | Normal | connect ADSCK to a DQS capable FPGA pin | laforge | 02/20/2016 08:23 PM | |
| 1471 | Feature | Rejected | High | implement and test FPGA flashing via sam3u | laforge | 02/20/2016 08:23 PM | |
| 1470 | Feature | Rejected | High | implement DFU bootloader in first 16k of flash | laforge | 02/20/2016 08:23 PM | |
| 1469 | Feature | Rejected | Normal | make sure sam3u chip id is present in Usb Serial Number | laforge | 02/20/2016 08:23 PM | |
| 1468 | Feature | Rejected | Normal | try to fit 2.5mm audio jack footprint (UART) on back side | laforge | 02/20/2016 08:23 PM | |
| 1467 | Feature | Rejected | Immediate | place "sysmocom" name into copper layer (back side) | laforge | 02/20/2016 08:23 PM | |
| 1466 | Feature | Rejected | High | add footprint of 2x5pin 1.27mm pitch SWD connector | laforge | 02/20/2016 08:23 PM | |