

General Description

The PEB 2084, QUAT-S, implements 4 × four-wire S/T interfaces used to link ISDN terminals to PBXs or PBX to NTs. The component switches B- and D-channels between the S/T and the ISDN Oriented Modular (IOM-2) interfaces, the latter being a standard backplane interface for the ISDN-basic access. Each of the four S/T-channels implemented in the device can be individually assigned for S₀- or T interface.

Applications

- Digital line cards for ISDN subscribers
- Digital line cards for ISDN-trunk lines
- Multiplexers
- Set-top-boxes

Type	Package
PEB 2084-H	P-MQFP-44-1 (SMD)

Features

- 4-channel S/T transceiver
- Full duplex 2B+D S/T transceiver according to CCITT I.430, ETSI 300.012 and ANSI T1.605
- Receive timing recovery according to selected operating mode
- Conversion of frame structure between S/T interface and IOM-2
- Access to S- and Q-bits of S/T interface
- Execution of test loops
- Support of star configurations (external common echo bit)
- Support of D-channel collision resolution in trunk mode
- Full support of arbiter concept of the ELIC (PEB 20550)
- Support of synchronization of cordless applications
- Low power consumption (< 100 mW)
- Loop length > 1.5 km
- JTAG-boundary scan test interface
- P-MQFP-44 package

